

Hong Kong Science and Technology Parks Corporation Probe and Test Development Centre - EDA Service

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EDA Tools

Cadence

	Product Name	Reference No.
1	Cadence(R) Design Framework II	CDN-111
2	Cadence Framework Integration Runtime Option	CDN-117
3	Cadence(R) Design Framework Integrator's Toolkit	CDN-12141
4	Virtuoso(R) Simulation Environment	CDN-206
5	Virtuoso(R) Schematic VHDL Interface	CDN-21060
6	Virtuoso(R) Schematic Editor Verilog(R) Interface	CDN-21400
7	Cadence(R) Simulation Analysis Environment (SimVision)	CDN-25010
8	Verilog(R)-XL Simulator	CDN-26000
9	Incisive Functional Safety Simulator	CDN-26262
10	Verifault(R)-XL simulator	CDN-26500
11	Verifault-XL(R) Slave Node License	CDN-26510
12	Virtuoso(R) Schematic Editor HSPICE Interface	CDN-276
13	Incisive Enterprise Simulator - L	CDN-29610
14	Incisive Enterprise Simulator - XL	CDN-29651
15	Enterprise Simulator - XL Interface for MTI	CDN-29661
16	Enterprise Simulator - XL Interface for VCS	CDN-29671
17	Digital Mixed Signal Option to IES	CDN-29710
18	Incisive Advanced Option	CDN-29851
19	Incisive Low-Power Simulation Option	CDN-29861
20	Virtuoso Digital Implementation	CDN-3002
21	Virtuoso Digital Implementation XL	CDN-3003
22	VDI-XL Block Capacity Option	CDN-3004
23	Virtuoso(R) Analog Oasis Run-Time Option	CDN-32100
24	Virtuoso(R) Spectre Model Interface Option	CDN-32501
25	Virtuoso(R) Analog HSPICE Interface Option	CDN-32760
26	Virtuoso UltraSim Simulator	CDN-33400
27	Spectre(R) RelXpert Reliability Simulator	CDN-33580
28	Spectre Characterization Simulator Option	CDN-3500
29	Spectre(R) Classic Simulator	CDN-38500
30	Spectre(R) - RF option for 38500 and 91050	CDN-38520
31	Virtuoso(R) AMS Designer Environment	CDN-70000
32	AMS Designer with Flexible Analog Simulation	CDN-70020
33	Spectre AMS Connector	CDN-70060
34	Assura(TM) Design Rule Checker	CDN-72110
35	Assura(TM) Layout Vs. Schematic Verifier	CDN-72120
36	Assura(TM) Graphical User Interface Option	CDN-72140
37	Assura(TM) Multiprocessor Option	CDN-72150
38	Cadence Yield Analyzer and Optimizer	CDN-74020
39	Cadence(R) SKILL Development Environment	CDN-900
40	Spectre Multi-mode Simulation	CDN-90004
41	Spectre(R) Accelerated Parallel Simulator	CDN-91050
42	Spectre(R) Power Option	CDN-91400
43	Spectre(R) CPU Accelerator Option	CDN-91500
44	Spectre Extensive Partitioned Simulator	CDN-91600
45	Spectre Electromigration and IR Drop Simulator 3 pack	CDN-91700
46	Virtuoso(R) EDIF 200 Reader	CDN-940
47	Virtuoso(R) EDIF 200 Writer	CDN-945
48	Virtuoso Advanced Node Framework	CDN-95011
49	Virtuoso(R) Schematic Editor L	CDN-95100
50	Virtuoso(R) Schematic Editor XL	CDN-95115
51	Virtuoso(R) Analog Design Environment L	CDN-95200
52	Virtuoso(R) Analog Design Environment XL	CDN-95210
53	Virtuoso(R) Analog Design Environment - GXL	CDN-95220
54	Token Control for Virtuoso(R) Analog Design Environment - GXL	CDN-95222
55	Virtuoso LDE Analyzer Option	CDN-95230
56	Virtuoso(R) ADE Explorer	CDN-95250
57	Virtuoso(R) Visualization & Analysis XL	CDN-95255
58	Virtuoso(R) ADE Assembler	CDN-95260
59	Virtuoso(R) Variation Option	CDN-95265
60	Virtuoso(R) ADE Verifier	CDN-95270
61	Virtuoso(R) Layout Suite L	CDN-95300
62	Virtuoso(R) Layout Suite XL	CDN-95310
63	Virtuoso(R) DFM Option	CDN-95311
64	Virtuoso(R) Layout Suite - GXL	CDN-95321
65	Token Control for Virtuoso(R) Layout Suite - GXL	CDN-95322
66	Virtuoso Layout Suite GXL	CDN-95323
67	Token Control for Virtuoso Layout Suite GXL	CDN-95324
68	Virtuoso Implementation Aware Design Option	CDN-95510
69	Virtuoso Advanced Node Option for Layout	CDN-95511
70	Virtuoso Advanced Node Option for Layout Standard	CDN-95512
71	Virtuoso Layout Suite EAD	CDN-95600
72	Virtuoso Mixed Signal Option for Layout	CDN-95710
73	Cadence(R) Physical Verification System Design Rule Checker XL	CDN-96210
74	Cadence(R) Physical Verification System Layout vs. Schematic Checker XL	CDN-96220
75	Cadence(R) Physical Verification System Programmable Electrical Rules Checker	CDN-96230
76	Cadence Physical Verification System Programmable Electrical Rules Checker XL	CDN-96235
77	Cadence(R) Physical Verification System Results Manager	CDN-96240

78	Cadence(R) Physical Verification System Design Analysis Option	CDN-96245
79	Cadence Physical Verification System QuickView Signoff Environment	CDN-96246
80	Cadence(R) Physical Verification System Constraint Validator	CDN-96300
81	Cadence Physical Verification System Constraint Validator XL	CDN-96305
82	Virtuoso Integrated Physical Verification System Advanced Analysis Option for IPVS (96400)	CDN-96310
83	Cadence Physical Verification System Advanced Analysis Option for PVS DRX XL (96210)	CDN-96320
84	Cadence(R) Physical Verification System Advanced Device Option	CDN-96330
85	Cadence(R) Physical Verification System Pattern Matching Option	CDN-96340
86	Cadence(R) Physical Verification System Mask Rule Check Option	CDN-96350
87	Virtuoso(R) Integrated Physical Verification System Option for Virtuoso Layout Suite (95300, 95310)	CDN-96400
88	Conformal - L (a.k.a Conformal ASIC)	CDN-CFM100
89	Conformal - XL (a.k.a Conformal Ultra)	CDN-CFM200
90	Conformal - GXL (a.k.a Conformal Custom)	CDN-CFM300
91	Conformal Constraint Designer - L	CDN-CFM401
92	Conformal Constraint Designer - XL	CDN-CFM421
93	CCD Multi-Constraint Check Option	CDN-CFM422
94	Conformal Low Power - XL	CDN-CFM500
95	Conformal Low Power - GXL	CDN-CFM550
96	Conformal ECO Designer	CDN-CFM620
97	Conformal ECO Designer - GXL	CDN-CFM650
98	Encounter Low Power GXL Option	CDN-EDS10
99	Encounter Clock Concurrent Optimization	CDN-EDS210
100	Encounter Advanced Node GXL Option	CDN-EDS30
101	Encounter DFM GXL Option	CDN-EDS50
102	First Encounter - XL (aka Cadence (R) First Encounter - GPS)	CDN-FE100GPS
103	First Encounter - L (aka First Encounter VIP)	CDN-FE80
104	Genus Synthesis Solution	CDN-GEN100
105	Genus Low Power Option	CDN-GEN30
106	Genus Physical Option	CDN-GEN40
107	Genus CPU Accelerator Option	CDN-GEN80
108	Incisive Enterprise Verifier - XL	CDN-IEV101
109	Incisive Coverage Unreachability App	CDN-IEV102
110	Innovus 7nm Option	CDN-INVS07
111	Innovus 10nm Option	CDN-INVS10
112	Innovus Implementation System	CDN-INVS100
113	Innovus 20/16/14nm Option	CDN-INVS20
114	Innovus Mixed Signal Option	CDN-INVS30
115	Innovus High Frequency Route Option	CDN-INVS35
116	Innovus Hierarchical Design Option	CDN-INVS40
117	Innovus DFM Option	CDN-INVS50
118	Innovus 3D-IC Option	CDN-INVS60
119	Innovus CPU Accelerator Option	CDN-INVS80
120	Innovus Implementation System - Basic	CDN-INVS95
121	Modus ATPG	CDN-MOD200
122	Modus DFT Option	CDN-MOD30
123	Modus Diagnostics & Trend Analysis	CDN-MOD300
124	Modus Memory BIST Option	CDN-MOD40
125	Modus Logic BIST Option	CDN-MOD50
126	Modus Hierarchical Option	CDN-MOD60
127	Cadence Quantus QRC Extraction - L	CDN-QRCX100
128	Cadence Quantus QRC Extraction - XL	CDN-QRCX300
129	Cadence Quantus QRC Advanced Analysis GXL Option	CDN-QRCX310
130	Cadence Quantus QRC Advanced Modeling GXL Option	CDN-QRCX320
131	Cadence Quantus QRC Display Technology Option	CDN-QRCX330
132	Cadence Quantus QRC Advanced Modeling20 GXL Option	CDN-QRCX520
133	Cadence Quantus QRC Advanced Node Modeling Option	CDN-QRCX530
134	Cadence Quantus QRC 7nm Option	CDN-QRCX700
135	Tempus Timing Signoff Solution L	CDN-TPS100
136	Tempus Timing Signoff Solution XL	CDN-TPS200
137	Tempus Timing Signoff Solution TSO	CDN-TPS300
138	Tempus Timing Signoff Solution MP	CDN-TPS400
139	Virtuoso Digital Signoff Timing Solution	CDN-VDS100
140	Virtuoso Digital Signoff Power Solution	CDN-VDS200
141	Virtuoso Digital Test	CDN-VDT100
142	Virtuoso(R) Power System L	CDN-VPS100
143	Voltus IC Power Integrity Solution - L (VTS-L)	CDN-VTS100
144	Voltus IC Power Integrity Solution - XL (VTS-XL)	CDN-VTS200
145	Voltus IC Power Integrity Solution Advanced Analysis GXL Option (VTS-AA)	CDN-VTS201
146	Voltus IC Power Integrity Solution - MP (VTS-MP) (Acceleration Option to Voltus IC XL (VTS200))	CDN-VTS300
147	Voltus-Fi Custom Power Integrity Solution - XL	CDN-VTS500